Significant growth in the scale, performance and complexity of data centre networks has created new challenges for designers. Network functions that traditionally required dedicated appliances are no longer able to keep pace with the required data rates in an economical fashion.

The Arista 7170B Series are second-generation purpose built, high density, fixed configuration 100GbE systems with up to 6.4 Tbps performance in a compact, low power form factor.

Built around a fully P4 programmable data plane, the packet processor architecture allows for complete definition of packet parsing, lookups, traffic scheduling, packet modification and traffic monitoring. The 7170 harnesses this flexibility to deploy complex functions, usually only available in CPU based appliances, at wire speed directly in the network layer providing orders of magnitude performance improvements and power/space reduction.

Arista provides multiple user selectable pipeline profiles that focus on use cases including high scale tunneling, address translation and server load balancing combined with scalable L2 and L3 resources and rich EOS features. The unique flexibility means new pipeline features and applications can be developed and deployed without replacing the underlying hardware.

The Arista 7170B-64C offers 64 QSFP ports for dense 40/100 GbE deployments and can support flexible combinations of 10, 25, 40, 50 and 100 GbE.
Model Overview

The **7170B-64C** is a 2RU system with 64 100G QSFP ports offering wire speed performance with an overall throughput up to 6.4 Tbps. Each QSFP port is capable of a choice of 40/100GbE, with support for up to 128 10/25G ports when using breakouts. Two integrated SFP+ ports provide support for additional lower speed 1/10G interfaces.

High Availability and Flexibility

The Arista 7170 Series were designed for flexible deployment and continuous operations with system wide monitoring of both hardware and software components, simple serviceability and provisioning to prevent single points of failure.

Key features include:
- AC and DC power supply options
- Front to Rear cooling with dynamic temperature control.
- Redundant, color coded, field serviceable power and cooling subsystems:
  - 1+1 hot-swappable platinum rated power supplies
  - N+1 hot-swappable fans
- Live software patching
- Self healing software with Stateful Fault Repair (SFR)

Programmable Architecture

The 7170B series offers a protocol independent switch architecture with a highly programmable packet pipeline that allows the addition of new protocols, encapsulation and tunneling features to the packet processor through simple software upgrades without changes to the underlying hardware. With up to 80 programmable stages, the 7170B series provides more than twice as much pipeline capacity as the previous generation 7170, enabling new profiles with more complex functionality and greater scalability.

Arista leverages P4 to define advanced forwarding profiles for the 7170 packet pipeline to deliver custom behavior, change lookups performed on the packets or rapidly develop and test new functionality. This flexibility provided by multiple forwarding profiles written in P4 and compiled into Arista EOS, addresses multiple real world requirements for scale, advanced telemetry, security and encapsulation and can be further customized to specific customer use-cases. The Enhanced feature license enables use of specialist profiles.
Match-Action Units

The 7170 series packet processor pipeline consists of identical blocks of pipeline stages or Match-Action Units. The pipeline program, known as a profile, defines the functions implemented by each Match-Action Unit, matching specific information extracted from packet headers and performing lookups in the resource tables which then define modifications and forwarding decisions. This architecture results in a configurable data plane with very high throughput, broad range of packet processing functionality and application visibility.

Flexible and Scalable Resources

Network scalability is directly impacted by a switch's forwarding tables. Programmable profiles enable the 7170 Series to provide flexibility on how memory resources are allocated among the different tables and features. The architecture allows for flexible and programmable allocation of available resources to achieve maximum efficiency for a given customer application. The unique level of pipeline programmability enables deployment of the 7170 Series in traditional roles with standard scale requirements as well as a wide range of use cases with unique resource requirements.

Intelligent Packet Buffers

The Arista 7170B Series switches offer up to 64 MB of unified packet buffer that is shared dynamically across all ports. The packet buffer can be allocated programmatically across multiple classes of applications to optimize performance for custom applications along with standard data center applications designed to avoid congestion from micro-bursts or fan-in packets. Unlike architectures with small per-port packet memory, or small shared memory pools the 7170 Series use a scheme to allocate memory intelligently based on a combination of traffic class, queue depth and quality of service policy ensuring fair allocation to all ports.

Application Profiles

The 7170 series with Arista EOS support a rich set of both data plane and control plane features and capabilities with a programmable SDK that address the deployment in either a leaf or spine role in two-tier networks. In addition, configurable application specific profiles provide a targeted set of data plane and control plane features with the feature scale tailored to address specific deployment scenarios.

Some examples of solutions the 7170 Series can enable:

- **Bare-metal Integration with Network Overlays and Virtualization:** Enables large scale integration of bare metal compute into virtualized overlays, offloading network functions such as traffic segmentation or tunnel encapsulation from bare metal servers freeing up compute resources and accelerating applications.

- **Large Scale Network Address Translation:** Provides highly scalable static and dynamic network address and port translation (NAT) at line rate with synchronization for gateway redundancy.

- **IPv4 to v6 transition with MAP-T:** Implements line-rate, high scale, stateless translation between IPv4 and IPv6 enabling service providers to remove IPv4 from core networks without the need for complex CGNAT deployments.

- **Stateless Load Balancing:** Delivers high bandwidth, low latency load balancing for distributed applications including cloud, web and containerized workloads.

- **Broadcast Media Workflows:** Provides in-network support for broadcast specific requirements including clean switching, stream reconstruction and advanced address translation.

For further information about available pipeline profiles or new profile development, please contact your Arista representative.
Layer 2 Features

- 802.1w Rapid Spanning Tree
- 802.1s Multiple Spanning Tree Protocol
- Rapid Per VLAN Spanning Tree (RPVST+)
- 4096 VLANs
- Q-in-Q
- 802.3ad Link Aggregation/LACP
  - 128 ports/channel
- Multi-Chassis Link Aggregation (MLAG)
  - 64 ports per MLAG
- 802.1AB Link Layer Discovery Protocol
- 802.3x Flow Control
- Jumbo Frames (9216 Bytes)
- IGMP v1/v2/v3 snooping
- Storm Control

Layer 3 Features

- Routing Protocols: OSPF, OSPFv3, BGP, MP-BGP, IS-IS, and RIPv2
- Static Routes
- Equal Cost Multipath Routing (ECMP)
- Resilient ECMP Routes
- VRF
- Bi-Directional Forwarding Detection (BFD)
- Route Maps
- IGMP v2/v3
- PIM-SM / PIM-SSM
- Anycast RP (RFC 4610)
- VRRP
- Virtual ARP (VARP)
- Policy Based Routing (PBR)
- uRPF
- Network Address Translation
  - Source/Destination NAT
- Source/Group Multicast NAT

Advanced Monitoring and Provisioning

- Zero Touch Provisioning (ZTP)
- Latency Analyzer and Microburst Detection (LANZ)
  - Configurable Congestion Notification (CLI, Syslog)
  - Streaming Events (GPB Encoded)
  - Capture/Mirror of congested traffic
- Advanced Monitoring and Aggregation
  - Port Mirroring
  - L2/3/4 Filtering on Mirror Sessions
  - Port Channel source and destination
  - Mirror to CPU
- Advanced Event Management suite (AEM)
  - CLI Scheduler
  - Event Manager
  - Event Monitor
  - Linux tools

• Integrated packet capture/analysis with TCPDump
• RFC 3176 sFlow
• Restore & configure from USB
• Blue Beacon LED for system identification
• eAPI
• OpenStack Neutron Support
• IEEE 1588 PTP (Transparent Clock and Boundary Clock)

Virtualization Support

- VXLAN Routing and Bridging
- VM Tracer VMware Integration

Security Features

- Control Plane Protection (CPP)
- Ingress ACLs using L2, L3, L4 fields
- ACL Deny Logging
- ACL Counters
- DHCP Relay / Snooping
- MACsec (802.1AE)
- TACACS+
- RADIUS

Quality of Service (QoS) Features

- Up to 8 queues per port
- 802.1p based classification
- DSCP based classification and remarking
- Explicit Congestion Notification (ECN)
- QoS interface trust (COS / DSCP)
- Strict priority queueing
- Weighted Round Robin (WRR) Scheduling
- Per-Priority Flow Control (PFC)
- Data Center Bridging Extensions (DCBX)
- 802.1Qaz Enhanced Transmissions Selection (ETS)
- Per port MMU Configuration
- Policing/Shaping
- Rate limiting

1. Due to the highly programmable nature of the packet processor, available functionality and scale varies by the current active profile
**Network Management**
- CloudVision
- Configuration rollback and commit
- 100/1000 Management Port
- RS-232 Serial Console Port
- USB Port
- SNMP v1, v2, v3
- Management over IPv6
- Telnet and SSHv2
- Syslog
- AAA
- Industry Standard CLI
- Beacon LED for system identification
- System Logging
- Environment monitoring

**Extensibility**
- Linux Tools
  - Bash shell access and scripting
  - RPM support
  - Custom kernel modules
- Software Defined Networking (SDN)
  - eAPI
  - OpenStack Neutron Support
- Programmatic access to system state
  - Python
  - Chef
  - Puppet
  - C++
  - eAPI
  - GO
  - OpenConfig
  - OpenStack Neutron Plug-in support
- Native KVM/QEMU support

**Standards Compliance**
- 802.1D Bridging and Spanning Tree
- 802.1p QOS/COS
- 802.1Q VLAN Tagging
- 802.1w Rapid Spanning Tree
- 802.1s Multiple Spanning Tree Protocol
- 802.1AB Link Layer Discovery Protocol
- 802.3ad Link Aggregation with LACP
- 802.3x Flow Control
- 802.3ab 1000BASE-T
- 802.3z Gigabit Ethernet
- 802.3ae 10 Gigabit Ethernet
- 802.3by 25 Gigabit Ethernet
- 802.3ba 40 Gigabit Ethernet
- 802.3ba 100 Gigabit Ethernet
- 802.3bs 200 Gigabit Ethernet
- RFC 2460 Internet Protocol, Version 6 (IPv6)
- RFC 2461 Neighbor Discovery for IP Version 6 (IPv6)
- RFC 2462 IPv6 Stateless Address Autoconfiguration
- RFC 2463 Internet Control Message Protocol (ICMPv6)
- IEEE 1588-2008 Precision Time Protocol

**SNMP MIBs**
- RFC 3635 EtherLike-MIB
- RFC 3418 SNMPv2-MIB
- RFC 2863 IF-MIB
- RFC 2864 IF-INVERTED-STACK-MIB
- RFC 2096 IP-FORWARD-MIB
- RFC 4363 Q-BRIDGE-MIB
- RFC 4188 BRIDGE-MIB
- RFC 2013 UDP-MIB
- RFC 2012 TCP-MIB
- RFC 2011 IP-MIB
- RFC 2790 HOST-RESOURCES-MIB
- RFC 3636 MAU-MIB
- RMON-MIB
- RMON2-MIB
- HC-RMON-MIB
- LLDP-MIB
- LLDP-EXT-DOT1-MIB
- LLDP-EXT-DOT3-MIB
- ENTITY-MIB
- ENTITY-SENSOR-MIB
- ENTITY-STATE-MIB
- ARISTA-ACL-MIB
- ARISTA-QUEUE-MIB
- RFC 4273 BGP4-MIB
- RFC 4750 OSPF-MIB
- ARISTA-CONFIG-MAN-MIB
- ARISTA-REDUNDANCY-MIB
- RFC 2787 VRRPv2MIB
- MSDP-MIB
- PIM-MIB
- IGMP-MIB
- IPMROUTE-STD-MIB
- SNMP Authentication Failure trap
- ENTITY-SENSOR-MIB support for DOM (Digital Optical Monitoring)
- User configurable custom OIDs

See EOS release notes for latest supported MIBs

1. Due to the highly programmable nature of the packet processor, available functionality and scale varies by the current active profile
## Specifications

<table>
<thead>
<tr>
<th>Switch Model</th>
<th>7170B-64C</th>
</tr>
</thead>
<tbody>
<tr>
<td>Ports</td>
<td>64x QSFP100 2x SFP</td>
</tr>
<tr>
<td>Max 100GbE Ports</td>
<td>64</td>
</tr>
<tr>
<td>Max 50GbE Ports</td>
<td>128</td>
</tr>
<tr>
<td>Max 40GbE Ports</td>
<td>64</td>
</tr>
<tr>
<td>Max 25GbE Ports</td>
<td>128</td>
</tr>
<tr>
<td>Max 10GbE Ports</td>
<td>130</td>
</tr>
<tr>
<td>Max 1GbE Ports</td>
<td>2</td>
</tr>
<tr>
<td>MACSec</td>
<td>All QSFP Ports</td>
</tr>
<tr>
<td>Throughput (Full Duplex)</td>
<td>6.4 Tbps (12.8 Tbps)</td>
</tr>
<tr>
<td>Packets/Second</td>
<td>Up to 3.2 Bpps</td>
</tr>
<tr>
<td>Latency</td>
<td>From 1 us</td>
</tr>
<tr>
<td>Programmable Pipeline Stages</td>
<td>Up to 80</td>
</tr>
<tr>
<td>CPU</td>
<td>Multi-core x86</td>
</tr>
<tr>
<td>System Memory</td>
<td>64 GB</td>
</tr>
<tr>
<td>Flash Storage Memory</td>
<td>120 GB</td>
</tr>
<tr>
<td>Packet Buffer Memory</td>
<td>64 MB</td>
</tr>
<tr>
<td>10/100/1000 Mgmt Ports</td>
<td>1</td>
</tr>
<tr>
<td>RS-232 Serial Ports</td>
<td>1 (RJ-45)</td>
</tr>
<tr>
<td>USB Ports</td>
<td>1</td>
</tr>
<tr>
<td>Hot-swap Power Supplies</td>
<td>2 (1+1 redundant)</td>
</tr>
<tr>
<td>Hot-swappable Fans</td>
<td>4 (N+1 redundant)</td>
</tr>
<tr>
<td>Reversible Airflow Option</td>
<td>Front to Rear only</td>
</tr>
<tr>
<td>Typical/Max Power Draw</td>
<td>810 W / 1230 W Max. (excl. transceivers)</td>
</tr>
<tr>
<td>Rack Units</td>
<td>2 U</td>
</tr>
<tr>
<td>Size (WxHxD)</td>
<td>17.55 x 3.46 x 23.42 inches (44.58 x 8.8 x 59.5 cm)</td>
</tr>
<tr>
<td>Weight</td>
<td>39.4 lbs (17.9 kg)</td>
</tr>
<tr>
<td>Power Supplies</td>
<td>PWR-2411-AC / PWR-2411-DC</td>
</tr>
<tr>
<td>EOS Feature Licenses</td>
<td>Group 3</td>
</tr>
<tr>
<td>Minimum EOS</td>
<td>TBC</td>
</tr>
</tbody>
</table>

1. Subject to support in EOS and pipeline profile installed
2. Performance rated over operation with average packets larger than 230 bytes with a medium complexity profile installed
3. Typical power consumption measured at 25°C ambient with 50% load
### Environmental Characteristics

<table>
<thead>
<tr>
<th>Feature</th>
<th>Specification</th>
</tr>
</thead>
<tbody>
<tr>
<td>Operating Temperature</td>
<td>0 to 40°C (32 to 104°F)</td>
</tr>
<tr>
<td>Storage Temperature</td>
<td>-40 to 70°C (-40 to 158°F)</td>
</tr>
<tr>
<td>Relative Humidity</td>
<td>5 to 95%</td>
</tr>
<tr>
<td>Operating Altitude</td>
<td>0 to 10,000 ft, (0-3,000m)</td>
</tr>
</tbody>
</table>

### Standards Compliance

#### EMC
- Emissions: FCC, EN55032, EN61000-3-2, EN61000-3-3

#### Immunity
- EN55024, EN55035
- EN300 386

#### Safety
- UL/CSA 60950-1, EN 62368-1, IEC-62368-1, IEC 60950-1
- CB Scheme with all country differences

#### Certifications
- North America (NRTL)
- European Union (EU)
- BSMI (Taiwan)
- C-Tick (Australia)
- CCC (PRC)
- KC (S. Korea)
- EAC (Eurasian Customs Union)
- VCCI (Japan)

### Power Supply Specifications

<table>
<thead>
<tr>
<th>Feature</th>
<th>PWR-2411-AC</th>
<th>PWR-2411-DC</th>
</tr>
</thead>
<tbody>
<tr>
<td>Input Voltage</td>
<td>200-240V AC</td>
<td>-48 to -60 VDC</td>
</tr>
<tr>
<td>Typical Input Current</td>
<td>14A</td>
<td>55A Max (-48V)</td>
</tr>
<tr>
<td>Input Frequency</td>
<td>50/60Hz</td>
<td>DC</td>
</tr>
<tr>
<td>Output Power</td>
<td>2400W</td>
<td>2400W</td>
</tr>
<tr>
<td>Input Connector</td>
<td>IEC 60320 C20</td>
<td>AWG #6 Max</td>
</tr>
<tr>
<td>Efficiency (Typical)</td>
<td>93% Platinum</td>
<td>94%</td>
</tr>
</tbody>
</table>

### European Union Directives
- 2006/95/EC Low Voltage Directive
- 2004/108/EC EMC Directive
- 2011/65/EU RoHS Directive
- 2012/19/EU WEEE Directive
Arista Optics and Cables


<table>
<thead>
<tr>
<th>Interface Type</th>
<th>40G QSFP ports</th>
</tr>
</thead>
<tbody>
<tr>
<td>10GBASE-CR</td>
<td>QSFP+ to 4xSFP+: 0.5m-5m lengths</td>
</tr>
<tr>
<td>40GBASE-CR4</td>
<td>QSFP+ to QSFP+: 0.5m-5m lengths</td>
</tr>
<tr>
<td>40GBASE-AOC</td>
<td>3m to 100m lengths</td>
</tr>
<tr>
<td>40GBASE-UNIV</td>
<td>150m OM3 / 150m OM4, 500m SM</td>
</tr>
<tr>
<td>40GBASE-SRBD</td>
<td>100m OM3 /150m OM4 Duplex MMF</td>
</tr>
<tr>
<td>40GBASE-SR4</td>
<td>100m OM3 /150m OM4 Parallel MMF</td>
</tr>
<tr>
<td>40GBASE-XSR4</td>
<td>300m OM3 /400m OM4 Parallel MMF</td>
</tr>
<tr>
<td>40GBASE-PLRL4</td>
<td>1km (1km 4x10G LR/LRL)</td>
</tr>
<tr>
<td>40GBASE-PLR4</td>
<td>10km (10km 4x10G LR/LRL)</td>
</tr>
<tr>
<td>40GBASE-LRL4</td>
<td>1km Duplex SM</td>
</tr>
<tr>
<td>40GBASE-LR4</td>
<td>10km Duplex SM</td>
</tr>
<tr>
<td>40GBASE-ER4</td>
<td>40km Duplex SM</td>
</tr>
</tbody>
</table>

100GbE

<table>
<thead>
<tr>
<th>Interface Type</th>
<th>100G QSFP ports</th>
</tr>
</thead>
<tbody>
<tr>
<td>10GBASE-SR4</td>
<td>70m OM3 / 100m OM4 Parallel MMF</td>
</tr>
<tr>
<td>10GBASE-XSR4</td>
<td>150m OM3 / 300m OM4 Parallel MMF</td>
</tr>
<tr>
<td>10GBASE-SWDM4</td>
<td>70m OM3 / 100m OM4 Duplex MMF</td>
</tr>
<tr>
<td>10GBASE-SRBD</td>
<td>70m OM3 / 100m OM4 Duplex MMF</td>
</tr>
<tr>
<td>10GBASE-LR</td>
<td>10km Duplex SM</td>
</tr>
<tr>
<td>10GBASE-LR4</td>
<td>10km Duplex SM</td>
</tr>
<tr>
<td>10GBASE-LRL4</td>
<td>2km Duplex SM</td>
</tr>
<tr>
<td>10GBASE-XCWD4</td>
<td>10km Duplex SM</td>
</tr>
<tr>
<td>10GBASE-CWDM4</td>
<td>2km Duplex SM</td>
</tr>
<tr>
<td>10GBASE-FR</td>
<td>2km Duplex SM</td>
</tr>
<tr>
<td>10GBASE-DR</td>
<td>500m Duplex SM</td>
</tr>
<tr>
<td>10GBASE-PSM4</td>
<td>500m Parallel SM</td>
</tr>
<tr>
<td>10GBASE-AOC</td>
<td>1m to 30m lengths</td>
</tr>
<tr>
<td>10GBASE-ERL4</td>
<td>40km Duplex SM</td>
</tr>
<tr>
<td>10GBASE-CR4</td>
<td>QSFP to QSFP: 1m to 5m lengths</td>
</tr>
<tr>
<td>50BASE-CR2</td>
<td>QSFP to 2xQSFP: 1m to 5m lengths</td>
</tr>
<tr>
<td>25BASE-CR</td>
<td>QSFP to SFP25: 1m to 5m lengths</td>
</tr>
</tbody>
</table>
### Product Number | Product Description
--- | ---
DCS-7170B-64C-F | Arista 7170B, Programmable 64 x 100GbE QSFP switch, front to rear air, 2 x AC
DCS-7170B-64C# | Arista 7170B, Programmable 64 x 100GbE QSFP switch, configurable fans and psu
LIC-FIX-3-E | Enhanced L3 License for Arista Group 3 Fixed switches, (BGP, OSPF, ISIS, PIM, NAT)
LIC-FIX-3-V | Virtualization license for Group 3 Arista Fixed switches (VMTracer and VXLAN)
LIC-FIX-3-V2 | EOS Extensions, Security and Partner Integration license for Arista Group 3 Fixed switches
LIC-FIX-3-Z | Monitoring & Automation license for Arista Group 3 Fixed switches (ZTP, LANZ, TapAgg, OpenFlow)
LIC-FIX-3-FLX-L | FLX-Lite License for Arista Fixed switches Group 3 - Full Routing Up to 256K Routes, EVPN, VXLAN, SR, base MPLS LSR (no TE or link/node protection)
LIC-FIX-3-FLX | FLX License for Arista Fixed switches Group 3 - Full Routing Up to 2M Routes, >24K ACL, EVPN, VXLAN, SR, Adv MPLS LSR with TE or link/node protection
LIC-FIX-ENH1 | Enhanced License Group 1 for 7170 platforms - All non-default profiles. Includes E, V2, Z, FLX.
LIC-FIX-3-MACSEC | MACsec Encryption License for Arista Group 3 Fixed switches, MACsec capable ports

### Optional Components and Spares

| Product Number | Description |
--- | ---
PWR-2411-AC-RED | Arista PSU, 1RU, AC/DC, 2400W, FORWARD, 73.5MM
PWR-2411-DC-RED | Arista PSU, 1RU, DC/DC, 2400W, FORWARD, 73.5MM
FAN-7012H-RED | Spare fan module for Arista 7000 Series 2RU High Speed Fan (front-to-read airflow)
KIT-7202 | Spare accessory kit for Arista 7170 2RU switches
KIT-GND-EXT-2RU | Arista 7000 Series 2RU Ground Extender Kit for NEBS compliance (7170B-64C)

### Warranty
The Arista 7170 series switches come with a one-year limited hardware warranty, which covers parts, repair, or replacement with a 10 business day turn-around after the unit is received.

### Service and Support
Support services including next business day and 4-hour advance hardware replacement are available. For service depot locations, please see: [http://www.arista.com/en/service](http://www.arista.com/en/service)

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